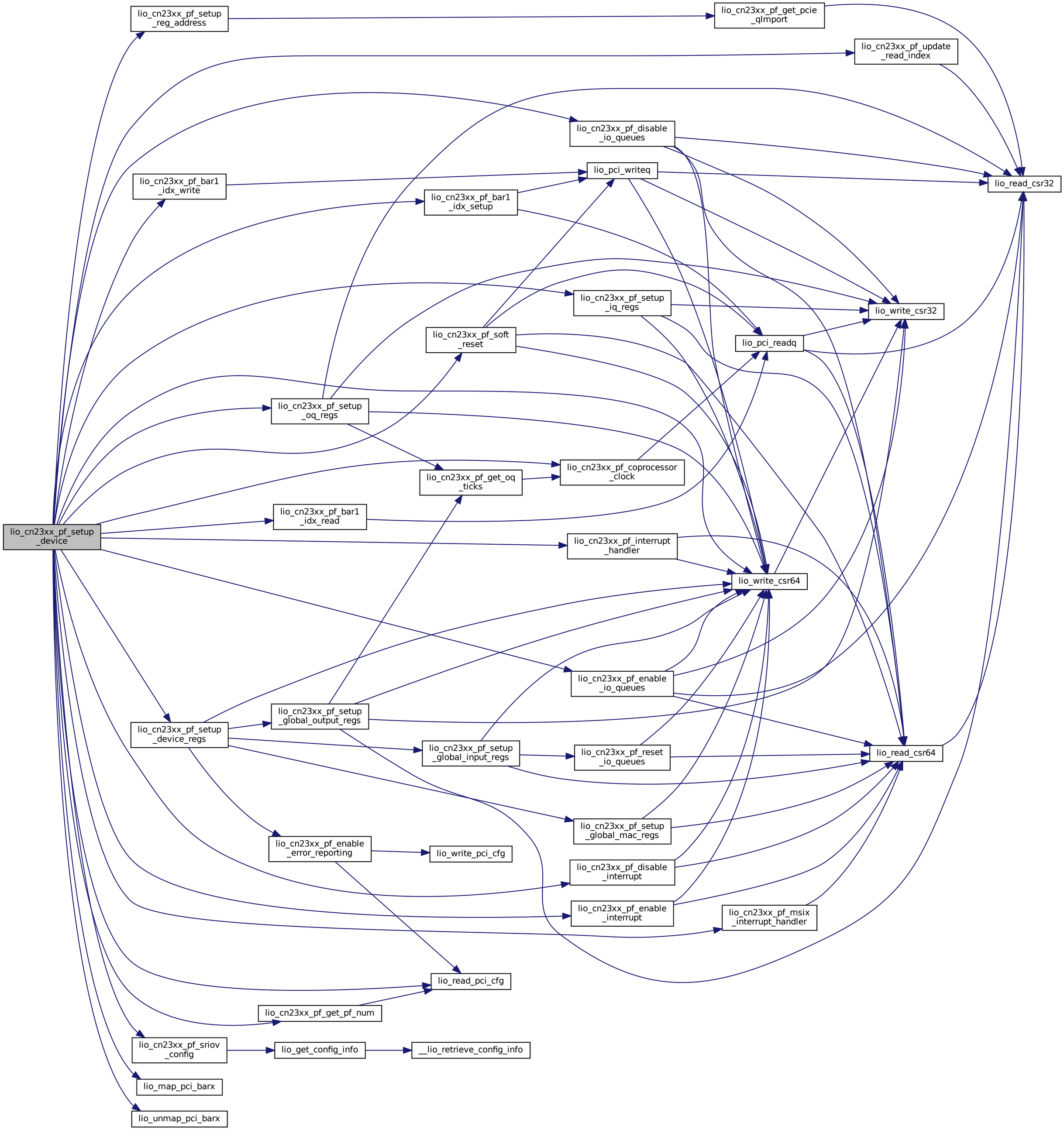
lio_cn23xx_pf_setup_reg_address
lio_cn23xx_pf_get_pcie_qlmport
lio_cn23xx_pf_update_read_index
lio_cn23xx_pf_disable_io_queues
lio_pci_writeq
lio_cn23xx_pf_bar1_idx_write
lio_read_csr32
lio_cn23xx_pf_bar1_idx_setup
lio_cn23xx_pf_setup_iq_regs
lio_write_csr32
lio_cn23xx_pf_soft_reset
lio_pci_readq
lio_cn23xx_pf_setup_oq_regs
lio_cn23xx_pf_coprocessor_clock
lio_cn23xx_pf_get_oq_ticks
lio_cn23xx_pf_bar1_idx_read
lio_cn23xx_pf_setup_device
lio_cn23xx_pf_interrupt_handler
lio_write_csr64
lio_cn23xx_pf_enable_io_queues
lio_cn23xx_pf_setup_global_output_regs
lio_cn23xx_pf_setup_device_regs
lio_cn23xx_pf_setup_global_input_regs
lio_cn23xx_pf_reset_io_queues
lio_read_csr64
lio_cn23xx_pf_setup_global_mac_regs
lio_cn23xx_pf_enable_error_reporting
lio_write_pci_cfg
lio_cn23xx_pf_disable_interrupt
lio_cn23xx_pf_enable_interrupt
lio_cn23xx_pf_msix_interrupt_handler
lio_read_pci_cfg
lio_cn23xx_pf_get_pf_num
lio_cn23xx_pf_sriov_config
lio_get_config_info
__lio_retrieve_config_info
lio_map_pci_barx
lio_unmap_pci_barx