

dev/ichiic/ig4_var.h

```
graph TD; A[dev/ichiic/ig4_var.h] --> B[bus_if.h]; A --> C[device_if.h]; A --> D[pci_if.h]; A --> E[iicbus_if.h];
```

bus_if.h

device_if.h

pci_if.h

iicbus_if.h