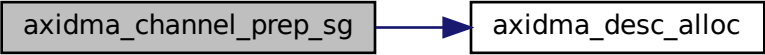


axidma_channel_prep_sg



```
graph LR; A[axidma_channel_prep_sg] --> B[axidma_desc_alloc]
```

axidma_desc_alloc