

vtpci_legacy_setup
_interrupts

vtpci_modern_setup
_interrupts

vtpci_setup_interrupts

```
graph LR; A[vtpci_legacy_setup_interrupts] --> C[vtpci_setup_interrupts]; B[vtpci_modern_setup_interrupts] --> C;
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The diagram illustrates a flow where two separate components, 'vtpci_legacy_setup_interrupts' and 'vtpci_modern_setup_interrupts', both point to a single target component, 'vtpci_setup_interrupts'. The target component is shaded gray, while the source components are white with black borders. Blue arrows indicate the direction of the flow from left to right.