

cp2112_gpio_pin_set

cp2112_gpio_pin_toggle

cp2112_gpio_write_pin

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graph LR; A[cp2112_gpio_pin_set] --> C[cp2112_gpio_write_pin]; B[cp2112_gpio_pin_toggle] --> C;
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The diagram illustrates a dependency or call relationship. Two boxes on the left, 'cp2112_gpio_pin_set' and 'cp2112_gpio_pin_toggle', have blue arrows pointing to a single box on the right, 'cp2112_gpio_write_pin'. The 'cp2112_gpio_write_pin' box is shaded gray, while the others are white with black borders.