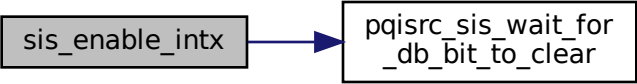


sis\_enable\_intx



```
graph LR; A[sis_enable_intx] --> B[pqisrc_sis_wait_for_db_bit_to_clear]
```

The diagram consists of two rectangular boxes connected by a blue arrow. The left box is shaded gray and contains the text 'sis\_enable\_intx'. A blue arrow points from the right side of this box to the left side of the right box. The right box is white with a black border and contains the text 'pqisrc\_sis\_wait\_for\_db\_bit\_to\_clear' on two lines.

pqisrc\_sis\_wait\_for  
\_db\_bit\_to\_clear