

dev/rtnw/rtl8192e/r92e  
\_var.h

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graph TD; A[dev/rtnw/rtl8192e/r92e_var.h] --> B[dev/rtnw/rtl8192e/r92e_rom_defs.h]; B --> C[dev/rtnw/rtl8192c/r92c_rom_defs.h];
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dev/rtnw/rtl8192e/r92e  
\_rom\_defs.h

dev/rtnw/rtl8192c/r92c  
\_rom\_defs.h