

dev/rtnw/rtl8812a/r12a
_reg.h

```
graph TD; A[dev/rtnw/rtl8812a/r12a_reg.h] --> B[dev/rtnw/rtl8188e/r88e_reg.h]; B --> C[dev/rtnw/rtl8192c/r92c_reg.h];
```

dev/rtnw/rtl8188e/r88e
_reg.h

dev/rtnw/rtl8192c/r92c
_reg.h