

qls\_rd\_xgmac\_reg

```
graph LR; A[qls_rd_xgmac_reg] --> B[qls_wait_reg_rdy]
```

A diagram showing a dependency between two registers. On the left, a gray rectangular box contains the text 'qls\_rd\_xgmac\_reg'. A blue arrow points from the right side of this box to the left side of a white rectangular box on the right, which contains the text 'qls\_wait\_reg\_rdy'. Both boxes have a black border.

qls\_wait\_reg\_rdy