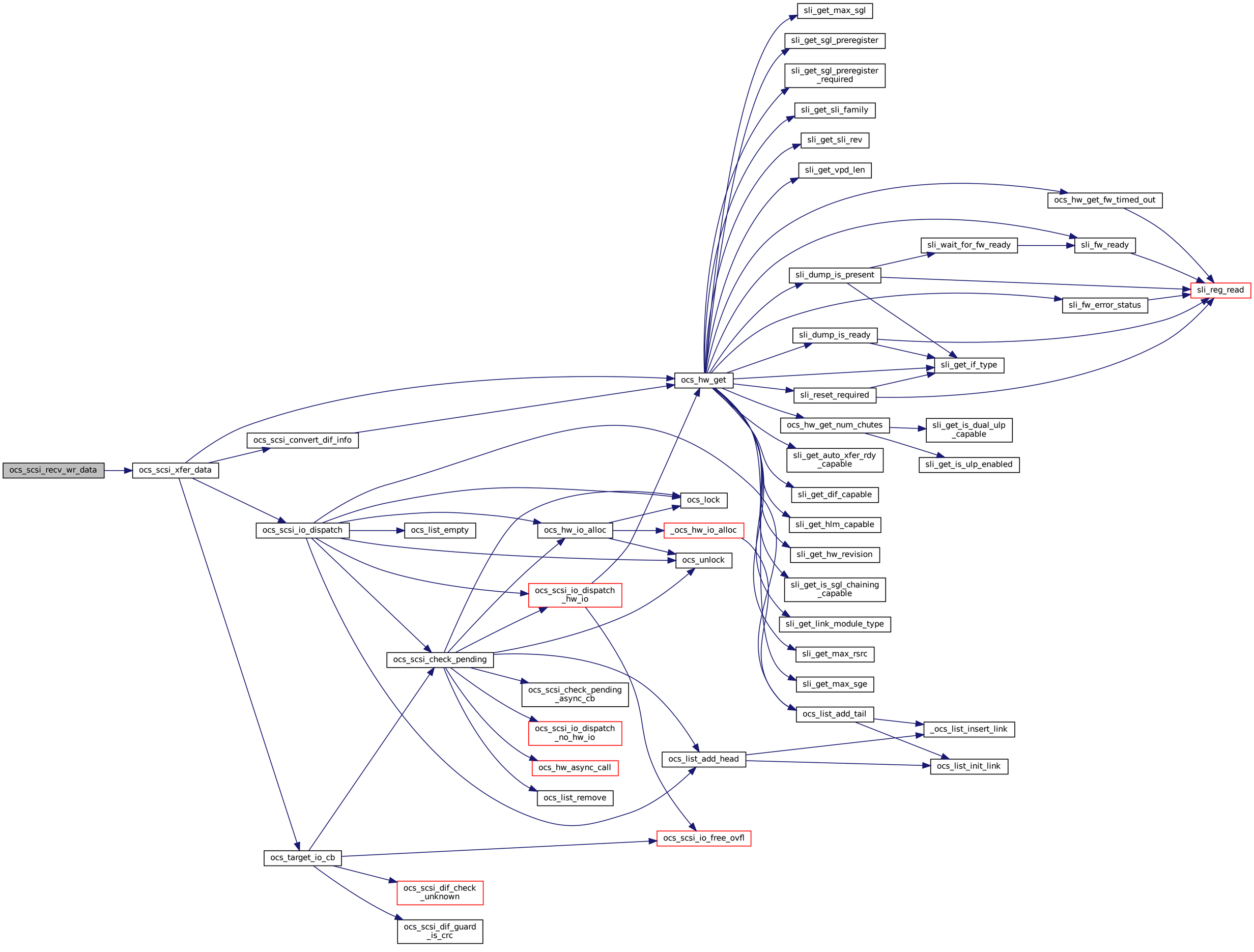
ocs_scsi_recv_wr_data
ocs_scsi_xfer_data
ocs_scsi_convert_dif_info
ocs_scsi_io_dispatch
ocs_list_empty
ocs_hw_io_alloc
_ocs_hw_io_alloc
ocs_lock
ocs_unlock
ocs_scsi_io_dispatch
_hw_io
ocs_scsi_check_pending
ocs_scsi_check_pending
_async_cb
ocs_scsi_io_dispatch
_no_hw_io
ocs_hw_async_call
ocs_list_remove
ocs_list_add_head
ocs_scsi_io_free_ovfl
ocs_target_io_cb
ocs_scsi_dif_check
_unknown
ocs_scsi_dif_guard
_is_crc
ocs_hw_get
sli_get_max_sgl
sli_get_sgl_preregister
sli_get_sgl_preregister
_required
sli_get_sli_family
sli_get_sli_rev
sli_get_vpd_len
ocs_hw_get_fw_timed_out
sli_wait_for_fw_ready
sli_fw_ready
sli_dump_is_present
sli_reg_read
sli_fw_error_status
sli_dump_is_ready
sli_get_if_type
sli_reset_required
ocs_hw_get_num_chutes
sli_get_is_dual_ulp
_capable
sli_get_is_ulp_enabled
sli_get_auto_xfer_rdy
_capable
sli_get_dif_capable
sli_get_hlm_capable
sli_get_hw_revision
sli_get_is_sgl_chaining
_capable
sli_get_link_module_type
sli_get_max_rsrc
sli_get_max_sge
ocs_list_add_tail
_ocs_list_insert_link
ocs_list_init_link