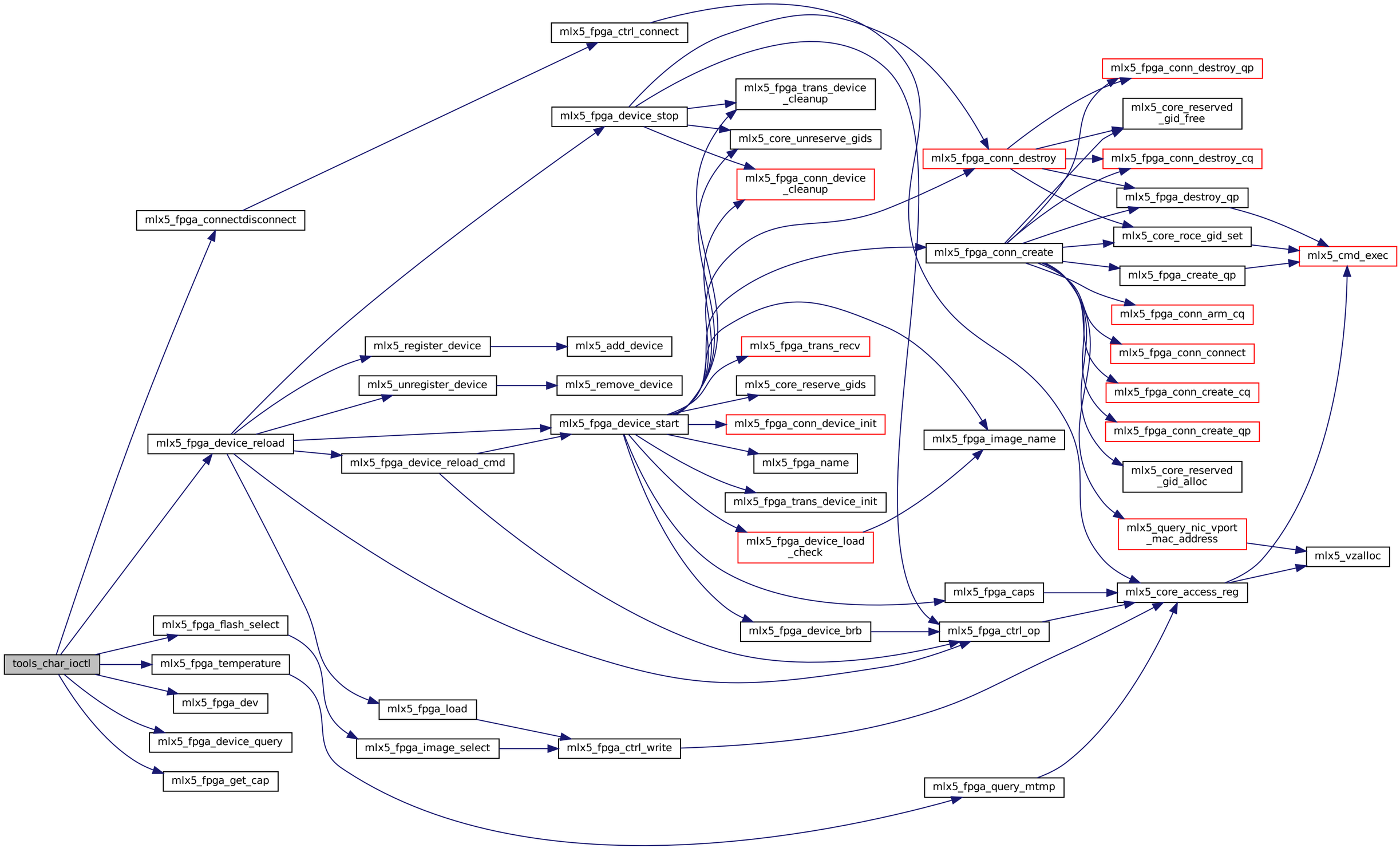
mlx5_fpga_ctrl_connect
mlx5_fpga_device_stop
mlx5_fpga_trans_device
_cleanup
mlx5_core_unreserve_gids
mlx5_fpga_conn_device
_cleanup
mlx5_fpga_connectdisconnect
mlx5_fpga_conn_destroy_qp
mlx5_core_reserved
_gid_free
mlx5_fpga_conn_destroy
mlx5_fpga_conn_destroy_cq
mlx5_fpga_destroy_qp
mlx5_core_roce_gid_set
mlx5_fpga_conn_create
mlx5_cmd_exec
mlx5_fpga_create_qp
mlx5_fpga_conn_arm_cq
mlx5_register_device
mlx5_add_device
mlx5_fpga_trans_recv
mlx5_fpga_conn_connect
mlx5_unregister_device
mlx5_remove_device
mlx5_core_reserve_gids
mlx5_fpga_conn_create_cq
mlx5_fpga_device_start
mlx5_fpga_conn_device_init
mlx5_fpga_conn_create_qp
mlx5_fpga_device_reload
mlx5_fpga_image_name
mlx5_fpga_device_reload_cmd
mlx5_fpga_name
mlx5_core_reserved
_gid_alloc
mlx5_fpga_trans_device_init
mlx5_query_nic_vport
_mac_address
mlx5_fpga_device_load
_check
mlx5_vzalloc
mlx5_fpga_caps
mlx5_core_access_reg
mlx5_fpga_flash_select
mlx5_fpga_device_brb
mlx5_fpga_ctrl_op
tools_char_ioctl
mlx5_fpga_temperature
mlx5_fpga_dev
mlx5_fpga_load
mlx5_fpga_device_query
mlx5_fpga_image_select
mlx5_fpga_ctrl_write
mlx5_fpga_get_cap
mlx5_fpga_query_mtmp