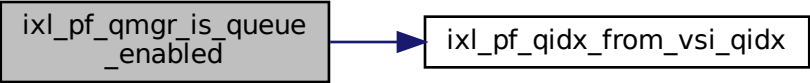


ixl\_pf\_qmgr\_is\_queue  
\_enabled



```
graph LR; A[ixl_pf_qmgr_is_queue_enabled] --> B[ixl_pf_qidx_from_vsi_qidx]
```

ixl\_pf\_qidx\_from\_vsi\_qidx