

ixgbe_dcb_config_pfc_cee

ixgbe_dcb_hw_config_cee

ixgbe_dcb_unpack_pfc_cee

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graph LR; A[ixgbe_dcb_config_pfc_cee] --> C[ixgbe_dcb_unpack_pfc_cee]; B[ixgbe_dcb_hw_config_cee] --> C;
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The diagram consists of three rectangular boxes. On the left, there are two boxes stacked vertically. The top box contains the text 'ixgbe_dcb_config_pfc_cee' and the bottom box contains 'ixgbe_dcb_hw_config_cee'. On the right, there is a single, wider box containing the text 'ixgbe_dcb_unpack_pfc_cee'. Two blue arrows originate from the right side of the top-left box and the right side of the bottom-left box, both pointing towards the left side of the rightmost box. The rightmost box has a light gray fill, while the other two are white with black borders.