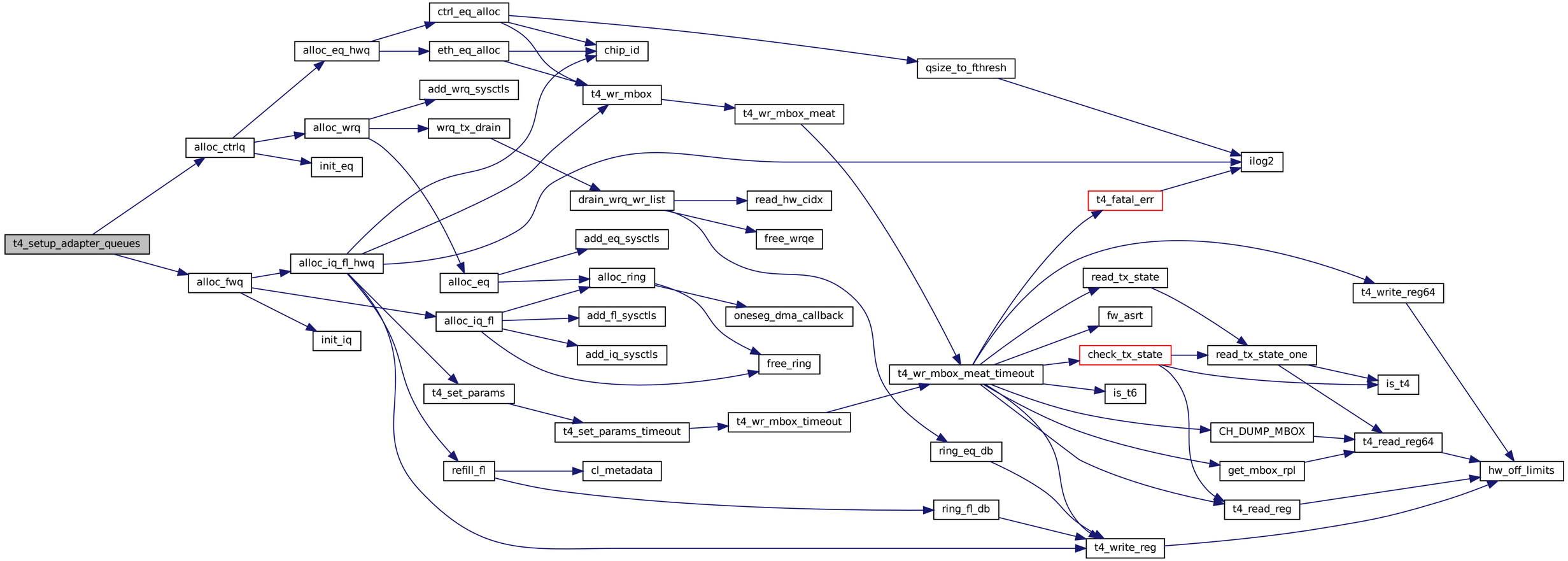
t4_setup_adapter_queues
alloc_ctrlq
alloc_fwq
alloc_eq_hwq
alloc_wrq
init_eq
alloc_iq_fl_hwq
init_iq
ctrl_eq_alloc
eth_eq_alloc
add_wrq_sysctls
wrq_tx_drain
alloc_eq
alloc_iq_fl
t4_set_params
refill_fl
chip_id
t4_wr_mbox
drain_wrq_wr_list
add_eq_sysctls
alloc_ring
add_fl_sysctls
add_iq_sysctls
t4_set_params_timeout
cl_metadata
t4_wr_mbox_meat
read_hw_cidx
free_wrqe
oneseg_dma_callback
free_ring
t4_wr_mbox_timeout
ring_eq_db
ring_fl_db
qsize_to_fthresh
ilog2
t4_fatal_err
t4_wr_mbox_meat_timeout
read_tx_state
fw_asrt
check_tx_state
is_t6
t4_write_reg64
read_tx_state_one
is_t4
CH_DUMP_MBOX
get_mbox_rpl
t4_read_reg64
t4_read_reg
hw_off_limits
t4_write_reg