

mv88e1xxx\_set\_speed  
\_duplex

vsc8211\_set\_speed\_duplex

t3\_set\_phy\_speed\_duplex

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graph LR; A[mv88e1xxx_set_speed_duplex] --> C[t3_set_phy_speed_duplex]; B[vsc8211_set_speed_duplex] --> C;
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The diagram illustrates a dependency or call relationship. Two source functions, 'mv88e1xxx\_set\_speed\_duplex' and 'vsc8211\_set\_speed\_duplex', are shown in white boxes on the left. Two blue arrows point from these boxes to a single target function, 't3\_set\_phy\_speed\_duplex', which is shown in a grey box on the right.