

ecore\_init\_vlan\_mac\_obj

ecore\_init\_vxlan\_fltr\_obj

ecore\_put\_credit\_vlan\_mac

```
graph LR; A[ecore_init_vlan_mac_obj] --> C[ecore_put_credit_vlan_mac]; B[ecore_init_vxlan_fltr_obj] --> C;
```

The diagram consists of three rectangular boxes. On the left, there are two boxes stacked vertically. The top box contains the text 'ecore\_init\_vlan\_mac\_obj' and the bottom box contains 'ecore\_init\_vxlan\_fltr\_obj'. On the right, there is a single, wider box containing the text 'ecore\_put\_credit\_vlan\_mac'. Two blue arrows originate from the right side of the left boxes and point towards the left side of the right box, indicating a flow or relationship from the left boxes to the right box.