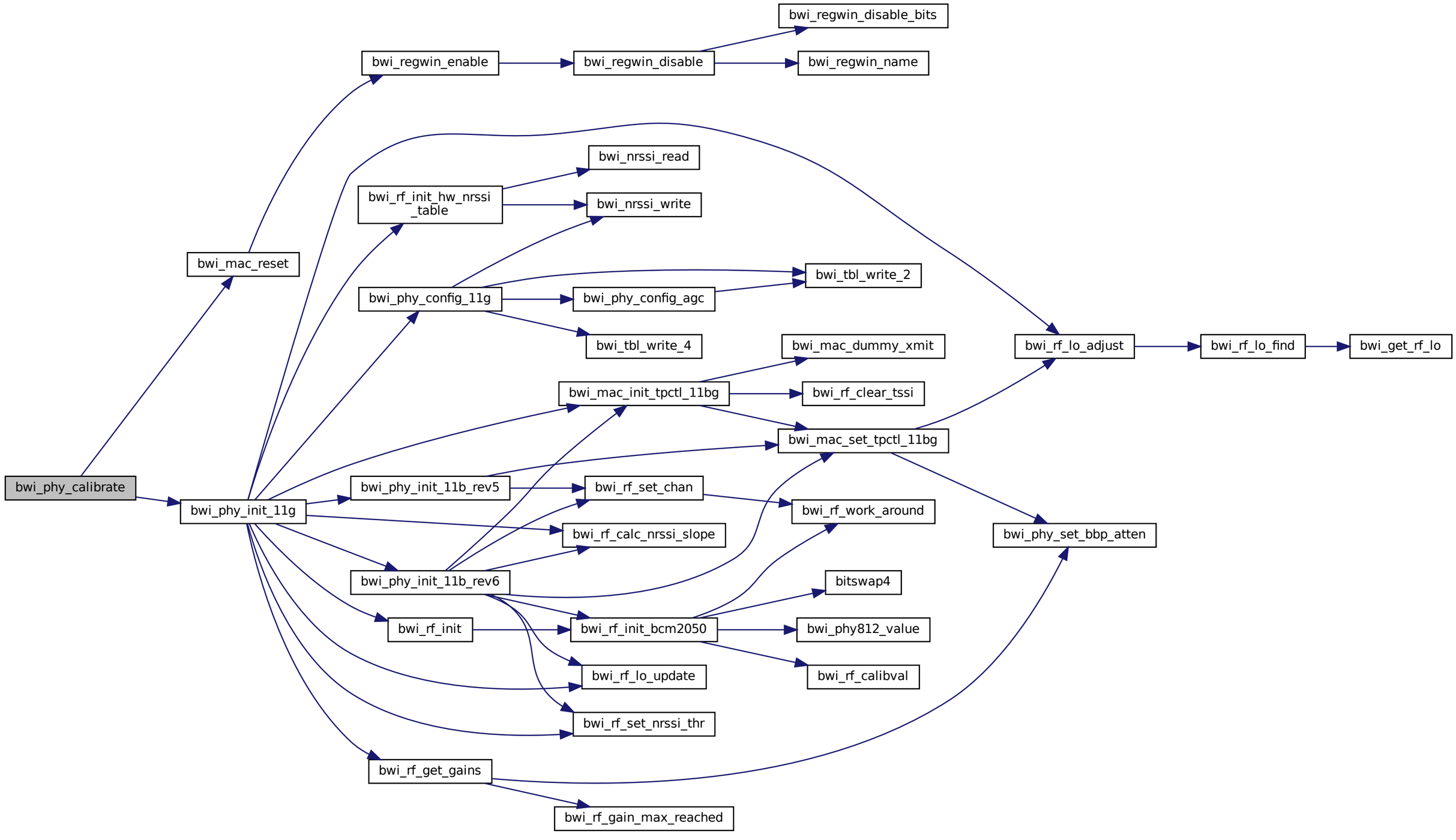
bwi_regwin_disable_bits
bwi_regwin_enable
bwi_regwin_disable
bwi_regwin_name
bwi_nrssi_read
bwi_rf_init_hw_nrssi
_table
bwi_nrssi_write
bwi_mac_reset
bwi_tbl_write_2
bwi_phy_config_11g
bwi_phy_config_agc
bwi_tbl_write_4
bwi_mac_dummy_xmit
bwi_rf_lo_adjust
bwi_rf_lo_find
bwi_get_rf_lo
bwi_mac_init_tpctl_11bg
bwi_rf_clear_tssi
bwi_mac_set_tpctl_11bg
bwi_phy_calibrate
bwi_phy_init_11g
bwi_phy_init_11b_rev5
bwi_rf_set_chan
bwi_rf_work_around
bwi_rf_calc_nrssi_slope
bwi_phy_set_bbp_atten
bwi_phy_init_11b_rev6
bitswap4
bwi_rf_init
bwi_rf_init_bcm2050
bwi_phy812_value
bwi_rf_lo_update
bwi_rf_calibval
bwi_rf_set_nrssi_thr
bwi_rf_get_gains
bwi_rf_gain_max_reached