


ar5416ConfigPCIE



```
graph LR; A[ar5416ConfigPCIE] --> B[ath_hal_ini_write]
```

The diagram consists of two rectangular boxes connected by a blue arrow. The left box is shaded gray and contains the text 'ar5416ConfigPCIE'. The right box is white and contains the text 'ath\_hal\_ini\_write'. A blue arrow points from the right side of the gray box to the left side of the white box.

ath\_hal\_ini\_write