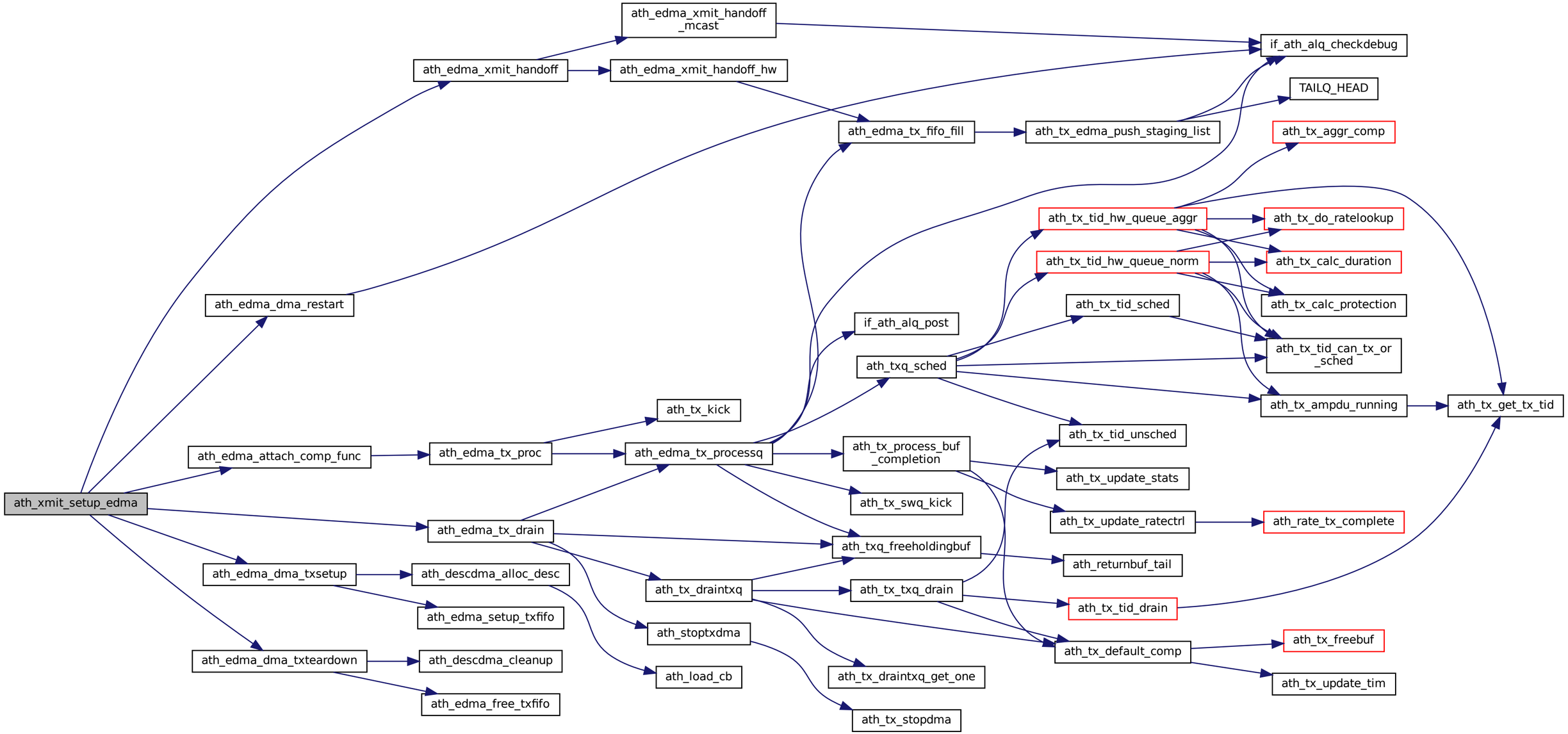
ath_xmit_setup_edma
ath_edma_dma_restart
ath_edma_xmit_handoff
ath_edma_xmit_handoff_mcast
ath_edma_xmit_handoff_hw
if_ath_alq_checkdebug
TAILQ_HEAD
ath_edma_tx_fifo_fill
ath_tx_edma_push_staging_list
ath_tx_aggr_comp
ath_tx_tid_hw_queue_aggr
ath_tx_do_ratelookup
ath_tx_tid_hw_queue_norm
ath_tx_calc_duration
ath_tx_calc_protection
ath_tx_tid_sched
if_ath_alq_post
ath_tx_tid_can_tx_or_sched
ath_txq_sched
ath_tx_ampdu_running
ath_tx_get_tx_tid
ath_tx_kick
ath_edma_attach_comp_func
ath_edma_tx_proc
ath_edma_tx_processq
ath_tx_process_buf_completion
ath_tx_tid_unsched
ath_tx_update_stats
ath_tx_swq_kick
ath_tx_update_ratectrl
ath_rate_tx_complete
ath_edma_tx_drain
ath_txq_freeholdingbuf
ath_returnbuf_tail
ath_edma_dma_txsetup
ath_descdma_alloc_desc
ath_tx_draintxq
ath_tx_txq_drain
ath_tx_tid_drain
ath_edma_setup_txfifo
ath_stoptxdma
ath_tx_default_comp
ath_tx_freebuf
ath_edma_dma_txteardown
ath_descdma_cleanup
ath_load_cb
ath_tx_draintxq_get_one
ath_tx_update_tim
ath_edma_free_txfifo
ath_tx_stopdma