


ar9287ConfigPCIE



```
graph LR; A[ar9287ConfigPCIE] --> B[ath_hal_ini_write]
```

The diagram consists of two rectangular boxes connected by a blue arrow. The left box is shaded gray and contains the text 'ar9287ConfigPCIE'. The right box is white and contains the text 'ath_hal_ini_write'. A blue arrow points from the right side of the left box to the left side of the right box.

ath_hal_ini_write