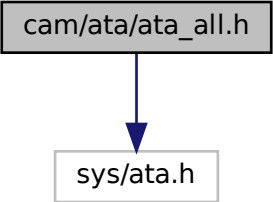


cam/ata/ata\_all.h



```
graph TD; A[cam/ata/ata_all.h] --> B[sys/ata.h]
```

sys/ata.h